

- 3-Terminal Regulators
- Output Current Up to 100 mA
- No External Components Required
- Internal Thermal-Overload Protection
- Internal Short-Circuit Current Limiting
- Provided Pb-Free packages from the end of 2004

#### description

79L08 (n.d) is fixed negative-voltage integrated-circuit voltage regulators is designed for a wide range of applications. These include on-card regulation for elimination of noise and distribution problems associated with single-point regulation. In addition,



they can be used to control series pass elements to make high-current voltage-regulator circuits. One of these regulators can deliver up to 100 mA of output current. The internal current-limiting and thermal-shutdown features make them essentially immune to overload. When used as a replacement for a zener-diode and resistor combination, these devices can provide effective improvement in output impedance of two orders of magnitude, with lower bias current.



#### equivalent schematic



### **Negative-Voltage Regulators**

Absolute maximum ratings over operating temperature range (unless otherwise noted) (TO-92)

ITEM	MAX RATINGS	UNIT
Input voltage	-30	V
Operating free-air, case, or virtual junctions temperature range	O to 150	
Storage temperature range	-65 to 150	°C
Lead temperature 1.6 mm (1/16 inch) from case for 10 seconds	260	

#### Recommended operating conditions

PARAMETER	MIN	MAX	UNIT
Input voltage Vi	10.5	-23	V
Output current, Io		40	mA
Operating virtual junction temperature, TJ	0	70	°C

Electrical characteristics at specified virtual junction temperature, V<sub>I=</sub> -14V, I<sub>o</sub>= 40mA (TO-92)

<b>ΔΑΡΔΜΕΤΕΡ</b>	TEST CONDITIONS*		79L08(n.d)			UNIT
			MIN	TYP	MAX	
Output voltage**		25°C	-7.7	-8	-8.3	V
	I <sub>0</sub> = 1mA to 40mA,		-7.6	-8	-8.4	
	V <sub>I</sub> = -10.5V to -23V	0°C to 125°C				
	I <sub>0</sub> =1mA to 70mA		-7.6	-8	-8.4	
Input regulation	V <sub>I</sub> = -10.5V to -23V	25°C		15	150	mV
	V <sub>I</sub> = -11V to -23V	23 0		12	100	
Ripple rejection	V <sub>I</sub> = -11V to -23V,	25°C	/1	10		dD
	f= 120Hz	23 0	41	49		uD
Output regulation	I <sub>0</sub> = 1mA to 100mA	25°C		20	60	m\/
	I <sub>0</sub> = 1mA to 40mA	23 0		10	30	111V
Output noise voltage	f= 10 Hz to 100 KHz	25°C		40		μV
Dropout voltage	I <sub>0</sub> =100mA	25°C		1.7		V
		25°C			6	mA
Bias current		125°C			5.5	
Bias current change	V <sub>I</sub> = -11V to -23V	0°C to 125°C			1.5	
	I <sub>0</sub> = 1mA to 40mA	0 0 10 120 0			0.1	

\* Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33µF capacitor across the input and a 0.1 µF capacitor across the output.
\*\* This specification applies only for dc power dissipation permitted by absolute maximum ratings.



## **Negative-Voltage Regulators**

CHIP APPEARANCE	CHIP SIZE			$0,85 \times 0,85 \text{ mm}$	
	CHIP THICKNESS			$460 \pm 20 \mu m$ (or $280 \pm 20 \mu m$ )	
	BONDING PAD DIMENSION	1	INPUT	$92  imes 92 \ \mu m$	
		2	OUTPUT	$92 \times 92 \ \mu m$	
		3	GROUND	$92  imes 92 \ \mu m$	
	SCRIBE LINE WIDTH			80 μm	
	TOP METAL			AI	
	BACK METAL			– (or Ti-Ni-Ag)	
	WAFER SIZE			100 mm	
-27 TT TT ALL I F					

# Chip name:79Lxx n.d